

ABSTRACT

ESD preventing-able level shifter, for receiving a first signal and outputting a second signal is provided. The level shifter comprises an inverter, a voltage converter, a first ESD clamp circuit and a second ESD clamp circuit. The inverter receives the first signal and outputs a first reverse signal. The voltage converter having a first input terminal for receiving the first reverse signal, a second input terminal for receiving the first signal and an output terminal for outputting the second signal. A first and second terminal of the first ESD clamp circuit is coupled to the first input terminal of the voltage converter and a second ground voltage, respectively. A first and a second terminal of the second ESD clamp circuit is coupled to the second input terminal of the voltage converter and the second ground voltage, respectively.